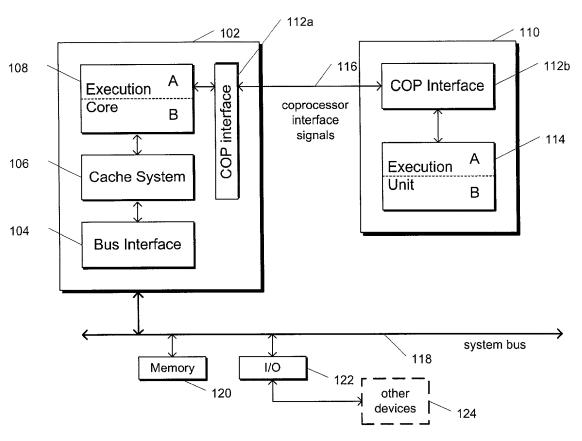
Figure 1



/ 100

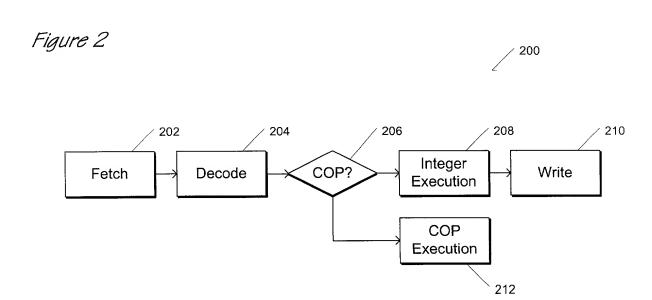
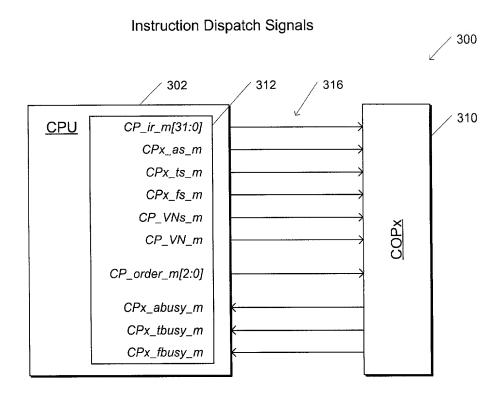


Figure 3



**/** 400

Figure 4

 $CP_VN_m$ 

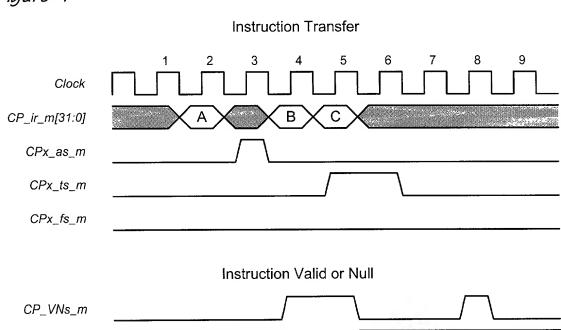


Figure 5

Instruction Transfer-Multiple Issue Groups

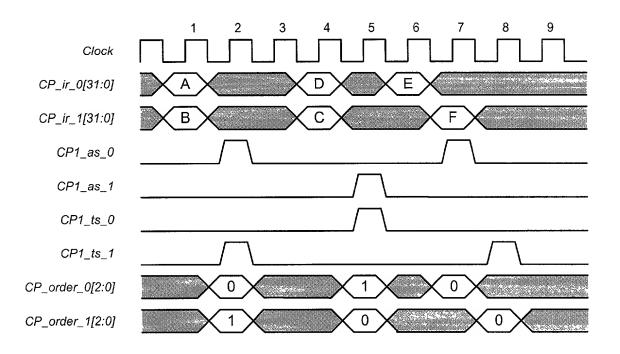


Figure 6
Instruction Transfer-COProcessor Busy

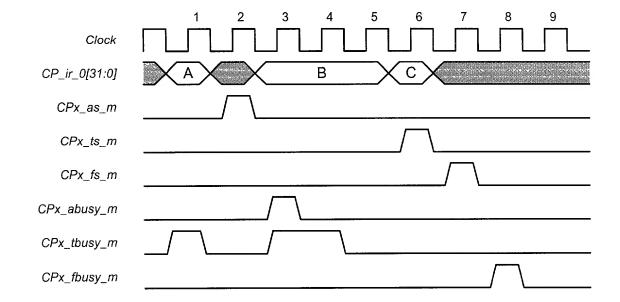
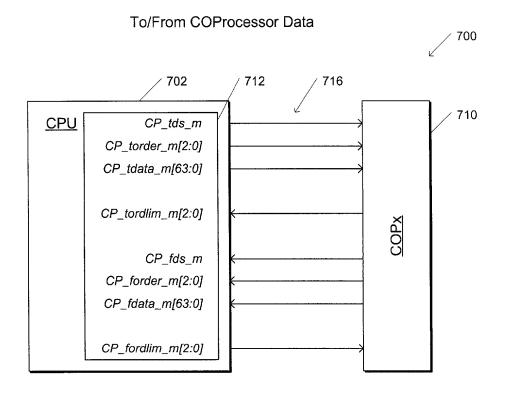


Figure 7



800

Figure 8

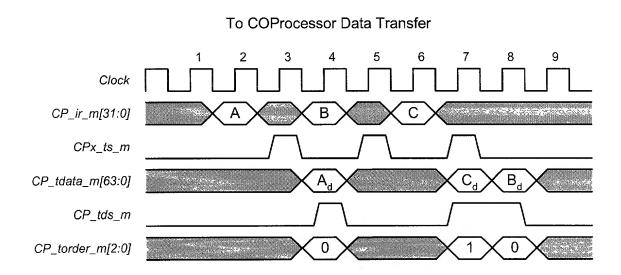


Figure 9



## From COProcessor Data Transfer

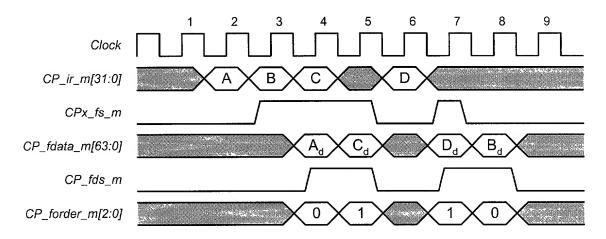


Figure 10

COProcessor Condition Code Check / GPR / exception Found / Instruction Commit

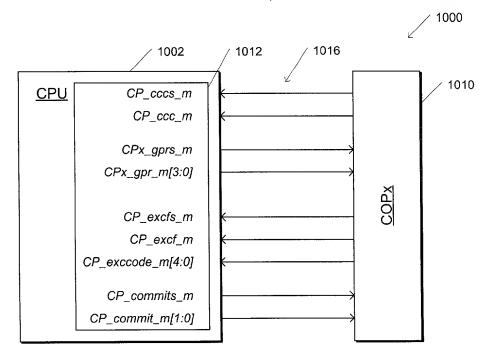


Figure 11

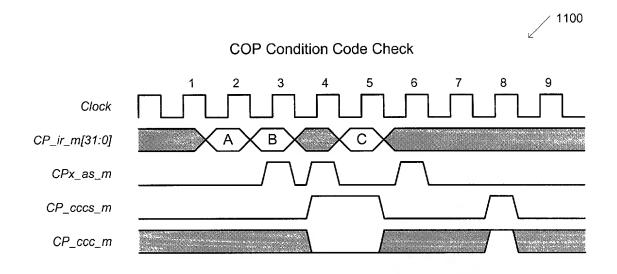
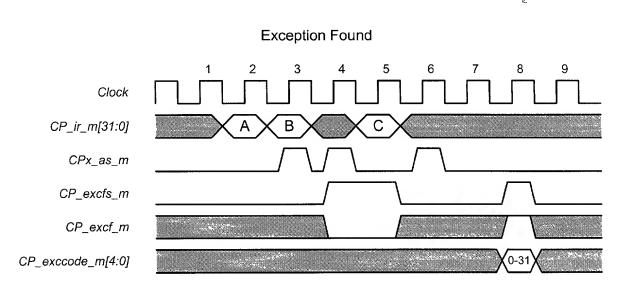


Figure 12



1200

Figure 13

